EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1589	"protected mode"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:32
L4	26	"64-bit MMX"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:33
L6	118	"SIMD registers"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:34
L8	30	"A20 gate"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:35
L9	3	"ADD and SUB"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:36
L10	3	"INC and DEC"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:36
L11	538	"boundary test"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:37
L12	1	"boundary test routine"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:37
L13	3	"stuck bit test"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:37
L15	428	"32-bit code"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:39
L16	1	MOVNTDQ	US-PGPUB; USPAT	OR	ON	2007/02/26 08:40
L18	1	"128-bit increments"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:48
L20	21	"128-bit SIMD registers"	US-PGPUB; USPAT	OR	ON	2007/02/26 09:10
L21	4903	"information handling system"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:54
L25	53	"1s and 0s"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:56
L26	4	"multi-pattern test"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:57
L28	145	"gate A20"	US-PGPUB; USPAT	OR	ON	2007/02/26 09:04
L31	322	Power-On-Self-Test	US-PGPUB; USPAT	OR	ON	2007/02/26 09:05
L32	46	"128-bit SIMD"	US-PGPUB; USPAT	OR	ON	2007/02/26 09:11
L34	71	"memory testing system"	US-PGPUB; USPAT	OR	ON	2007/02/26 09:13
L35	54438	"information system"	US-PGPUB; USPAT	OR	ON	2007/02/26 09:14

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L25	53	"1s and 0s"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:56
L26	4	"multi-pattern test"	US-PGPUB; USPAT	OR	ON	2007/02/26 08:57
L28	145	"gate A20"	US-PGPUB; USPAT	OR	ON	2007/02/26 09:04
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